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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/128,251 08/03/1998		IZUMI MATSUI	B208-975	B208-975 2424		
26272	7590	03/28/2002				
ROBIN BL		& DALEY	EXAMINER			
2ND FLOOI 330 MADIS	ON AVE	· 	CHIEU, PO LIN			
NEW YORK, NY 10017				ART UNIT	PAPER NUMBER	
			•	2615	2615	
				DATE MAIL ED: 03/28/2002	DATE MAIL ED: 03/28/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

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·	Application No.	plicant(s)					
Office Action Summers	09/128,251	MATSUI, IZUMI					
Office Action Summary	Examiner	Art Unit					
The MAII INC DATE of this communication and	Polin Chieu	2615					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status							
1) Responsive to communication(s) filed on 9 Ja	anuary 2002						
2a)⊠ This action is FINAL . 2b)☐ Th	is action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims	Ex parte Quayle, 1935 C.D. 11, 4	403 U.G. 213.					
4) Claim(s) 1-34 is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-34</u> is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/o	r election requirement.						
Application Papers							
9) The specification is objected to by the Examiner.							
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11) The proposed drawing correction filed on	• • •	• • •					
If approved, corrected drawings are required in reply to this Office action.							
12) The oath or declaration is objected to by the Examiner.							
Priority under 35 U.S.C. §§ 119 and 120							
13)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) ☐ All b) ☐ Some * c) ⊠ None of:							
1. Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
14) Acknowledgment is made of a claim for domestic	14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal I	(PTO-413) Paper No(s) Patent Application (PTO-152)					

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DETAILED ACTION

Response to Arguments

1. Applicant's arguments with respect to claims 1-34 have been considered but are most in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-6, 12-16, 20, and 29-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamagata (5,764,800) in view of Parulski et al (5,633,678) and Jeong (6,130,988).

Regarding claims 1, 12, 20 and 29, Yamagata discloses an inputting means (35) in figure 1 for inputting a video signal; a compressing means (also considered a processing means, 44) for compressing a video signal using a memory (M) in figure 2; a memory (M) and a recording means for recording on a recording medium (M) that has been compressed (col. 4, lines 4-16) by the compressing means (44) in figure 2; and an expanding means (44) for a video signal in figure 2. However, Yamagata does not disclose a generating means; and a memory means having a common memory for storing the video signal to perform a compression process and storing the character

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signal generated by the generating means to perform a combining operation of the character signal.

Parulski et al teaches a generating means generating character information using the memory (col. 2, lines 15-30).

The memory means of Yamagata and Parulski et al perform the compression process and the character generation process; however, the memory is not a single memory. Jeong teaches partitioning a single memory into several parts in figure 3. It would have been obvious to partition a memory mean to allow the memory to be used in a compression process and a character generation combining process.

It would have been highly desirable to have a generating means so that the users could add text to the images. It would have been highly desirable to partition the memory to allow compression and character generation to be done using a single memory thereby reducing the number of parts needed for the device.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a character generator and a video signal in Yamagata.

Regarding claim 2, Yamagata does not disclose a combining means for combining the character signal and the video signal.

Parulski et al teaches overlaying text on an image (col. 4, lines 55-67), so a combining means must be inherent.

It would have been highly desirable to have a combining means so that text could be overlaid on an image.

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Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention a combining means in Yamagata.

Regarding claim 3, Yamagata does not disclose that the compressing means compresses a video signal output from the combining means.

Paruluski et al teaches, in an alternate embodiment, that the output of the combining means (20) combining the character signal (20a) and the image signal (18) is compressed (22) in figure 2.

It would have been highly desirable to have a combining means so that the text data is compressed with the image data.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to have the compressing done after combining in Yamagata.

Regarding claim 4, Yamagata discloses an outputting means (42) for outputting the compressed video signal to a recording device (42) to be recorded on a recording medium (M) in figure 2.

Regarding claim 5, Yamagata discloses an outputting device (43) for displaying the video signal in figure 2.

Regarding claim 6, Yamagata discloses an image pickup means (21) in figure 2. Further, as discussed in the art rejection of claim 2, Parulski et al discloses a combining means combining a character signal with an image.

The limitations disclosed in claim 13 were discussed in the art rejection of claim 2. Please refer to the art rejection of claim 2.

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Regarding claim 14, Yamagata does not disclose combining the video signal with the expanded video signal.

Parulski et al teaches a combining means as discussed in the art rejection of claim 2.

It would have been highly desirable to combine the character signal with the expanded video signal so that text could be overlaid.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a combining means in Yamagata.

The limitations disclosed in claim 15 were discussed in the art rejection of claim 4. Note that the compression means (44) is the same as the expansion means (44) in figure 2 of Yamagata. Please refer to the art rejection of claim 4.

Regarding claim 16, Yamagata discloses an inputting means for inputting a signal reproduced from a recording medium (M) and writing the reproduced video signal into the memory (M) in figure 2 (col. 4, lines 4-16). Yamagata further discloses expanding the signal (col. 4, lines 4-16).

The limitations recited in claim 30 were discussed in the art rejection of claim 2. Please refer to the art rejection of claim 2.

Regarding claim 31, the compressing means compressing an amount of information of a combined video signal outputted from the combining means was discussed in the art rejection of claim 3. Yamagata discloses a recording means in (col. 4, lines 4-16).

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Regarding claim 32, Yamagata discloses a displaying means (col. 3, lines 26-31).

Regarding claim 33, Yamagata discloses a reproducing from a recording medium (M) the amount that has been compressed, and writing the reproduced signal into the memory (M) in figure 2 (col. 4, lines 4-16).

Regarding claim 34, the expanding means was discussed in the art rejection of claim 12, and the combining means was discussed in the art rejection of claim 14.

Please refer to the art rejections of claims 12 and 14.

4. Claims 7, 8, and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamagata in view of Parulski et al, Jeong, and Ota.

Regarding claim 7, Yamagata discloses a memory with a first area for a video signal to be compressed in figure 3. However, Yamagata does not disclose a second area for a compressed video signal and a third area used to generate the character signal.

Parulski et al teaches a memory with a first area (24a) and a third area (24b) used to generate the character signal in figure 2 (col. 5, lines 9-31).

Ota teaches a memory with a first area (14) and a second area for storing a compressed signal (15) in figure 1.

It would have been highly desirable to have a third area so that the user could store category information (col. 5, 9-31). It would have been highly desirable to store a compressed signal (or thumbnail image) so that the user could quickly see the contents of the memory.

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Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a second area and a third area in Yamagata.

Regarding claim 8, Yamagata discloses an outputting means for reading out from the second area, and having a recording device record on a recording medium (col. 4, lines 4-16).

Regarding claim 17, Yamagata discloses a memory with a first area for a video signal that has been expanded in figure 3. However, Yamagata does not disclose a second area for a video signal to be expanded and a third area used to generate the character signal.

Parulski et al teaches a memory with a first area (24a) and a third area (24b) used to generate the character signal in figure 2 (col. 5, lines 9-31).

Ota teaches a memory with a first area (14) and a second area for storing a signal (15) to be expanded in figure 1.

It would have been highly desirable to have a third area so that the user could store category information (col. 5, 9-31). It would have been highly desirable to store an image to be expanded (or thumbnail image) so that the user could quickly see the contents of the memory.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a second area and a third area in Yamagata.

5. Claims 9, 10, 18, and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamagata in view of Parulski et al, Jeong, Ota, and Honda.

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Yamagata discloses a first area to be accessed by the compression means.

However, Yamagata does not disclose a second area corresponding to an image plane represented by the video signal; that the generating means comprises memory control means for writing into the second area a plurality of codes representing a value of pixel data of the character signal; and a table for outputting pixel data corresponding to codes read out from said second area.

As discussed in the art rejection of claim 7, Parulski et al teaches a second area (discloses as the third area in claim 7) in which a character signal is stored. Clearly the character signal must also store information on where the character data will be placed in the image plane; therefore, it is inherent that the second area corresponds to an image plane represented by the video signal. Further Parulski et al teaches generating character data (col. 2, lines 4-30), which must represent a value of pixel data, and storing it into a second area (24b) in figure 2. Parulski et al does not disclose a table for outputting pixel data corresponding to the codes read out from the second area.

Honda teaches a table (17) outputting pixel data in figure 2. Further, since the table creates the text to be overlaid on the image the pixel data must correspond to the codes read out from the second area.

It would have been highly desirable to have a second area in which pixel data is written so that text can be overlaid on an image. In addition, a table would be desirable so that a table of text could be stored and easily generated.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a second area and a table in Yamagata.

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Regarding claim 10, the combining means was discussed in the art rejection of claim 2. Yamagata does not disclose codes representing control data.

Parulski et al teaches a combining means, as discussed in claim 2, for combining character and image data. Parulski et al teaches storing the character (24b) and image (24a) data separately in figure 2. Therefore, there must be codes representing control data for controlling the combining operation of the combining means, wherein the combining means performs the combining operation in accordance with the codes for the device to perform the overlaying of text onto an image as disclosed.

It would have been highly desirable to have a combining means and codes representing control data so that text could be overlaid on an image.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to have a combining means and codes representing control data in Yamagata.

Claims 18 and 19 are considered to be similar to claims 9 and 10; however, expansion occurs in place of compression. Since Yamagata discloses an expansion device (44) in figure 4, the art rejections would be similar to the art rejections of claims 9 and 10. Please refer to the art rejections of claim 9 and 10.

6. Claims 11 and 21-28 rejected under 35 U.S.C. 103(a) as being unpatentable over Yamagata in view of Parulski et al, Jeong, and Yonemitsu et al.

Regarding claim 11, Yamagata does not disclose an orthogonal transform means, a quantization means, and a variable-length coding (VLC) means.

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Yonemitsu et al teaches an orthogonal transform means (23), a quantization means (24), and a VLC means (25) in figure 23.

Orthogonal transform, quantization, and VLC are all well known coding methods in the art that are often used for compression. It would have been highly desirable to use the coding methods for compression to reduce the amount of data.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to have an orthogonal transform means, a quantization means, and a VLC means in Yamagata.

Regarding claim 21, the limitation of claim 11 discloses a high efficiency encoding means. Please refer to the art rejection of claim 11.

Regarding claim 22, compression is considered to be a step in high-efficiency encoding. The limitations of claim 7 disclose a first area to be accessed for compression, and a second area (note that it is called the third area in claim 7) for generating a character signal. Please refer to the art rejection of claim 7.

Regarding claim 23, Yamagata does not disclose error-correction-encoding. Jeong teaches error-correction-encoding (col. 4, lines 5-50).

It would have been highly desirable to have error-correction-coding so that errors are corrected.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have error-correction coding in Yamagata.

Regarding claim 24, Yamagata discloses a memory with a first area for a video signal to be accessed by the high-efficiency encoding means in figure 3. However,

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Yamagata does not disclose a second area to be accessed by and error correction encoding means and a third area used to generate the character signal.

Parulski et al teaches a memory with a first area (24a) and a third area (24b) used to generate the character signal in figure 2 (col. 5, lines 9-31).

Jeong teaches a second area (310) to be accessed by the error correction encoding means (col. 4, lines 5-50) in figure 3.

It would have been highly desirable to have a third area so that the user could store category information (col. 5, 9-31). It would have been highly desirable to have a second area for error correction so that errors could be corrected in the image.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a second area and a third area in Yamagata.

Regarding claim 25, the limitation of claim 11 discloses a high efficiency encoding means. If the apparatus had the high efficiency encoding means of claim 11 it would also need a high efficiency decoding means to properly process the data.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to have a high efficiency decoder in Yamagata.

Regarding claim 26, expansion is considered to be a step in high-efficiency decoding. The limitations of claim 17 disclose a first area to be accessed for expansion, and a second area (note that it is called the third area in claim 7) for generating a character signal. Please refer to the art rejection of claim 17.

Regarding claim 27, Yamagata does not disclose error-correction-decoding.

Jeong teaches error-correction-decoding (col. 4, lines 5-50).

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It would have been highly desirable to have error-correction-decoding so that errors are corrected.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have error-correction coding in Yamagata.

Regarding claim 28, Yamagata discloses a memory with a first area for a video signal to be accessed by the high-efficiency decoding means in figure 3. However, Yamagata does not disclose a second area to be accessed by and error correction decoding means and a third area used to generate the character signal.

Parulski et al teaches a memory with a first area (24a) and a third area (24b) used to generate the character signal in figure 2 (col. 5, lines 9-31).

Jeong discloses a second area (310) to be accessed by the error correction decoding means (col. 4, lines 5-50) in figure 3.

It would have been highly desirable to have a third area so that the user could store category information (col. 5, 9-31). It would have been highly desirable to have a second area for error correction so that errors could be corrected in the image.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention have a second area and a third area in Yamagata.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

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§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Polin Chieu whose telephone number is (703) 308-6070. The examiner can normally be reached on M-F 8:30 AM-6:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew B. Christensen can be reached on (703) 308-9644. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9314 for regular communications and (703) 872-9314 for After Final communications.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

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Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

PC March 22, 2002

ANDREW B. CHRISTENSEN PRIMARY EXAMINER